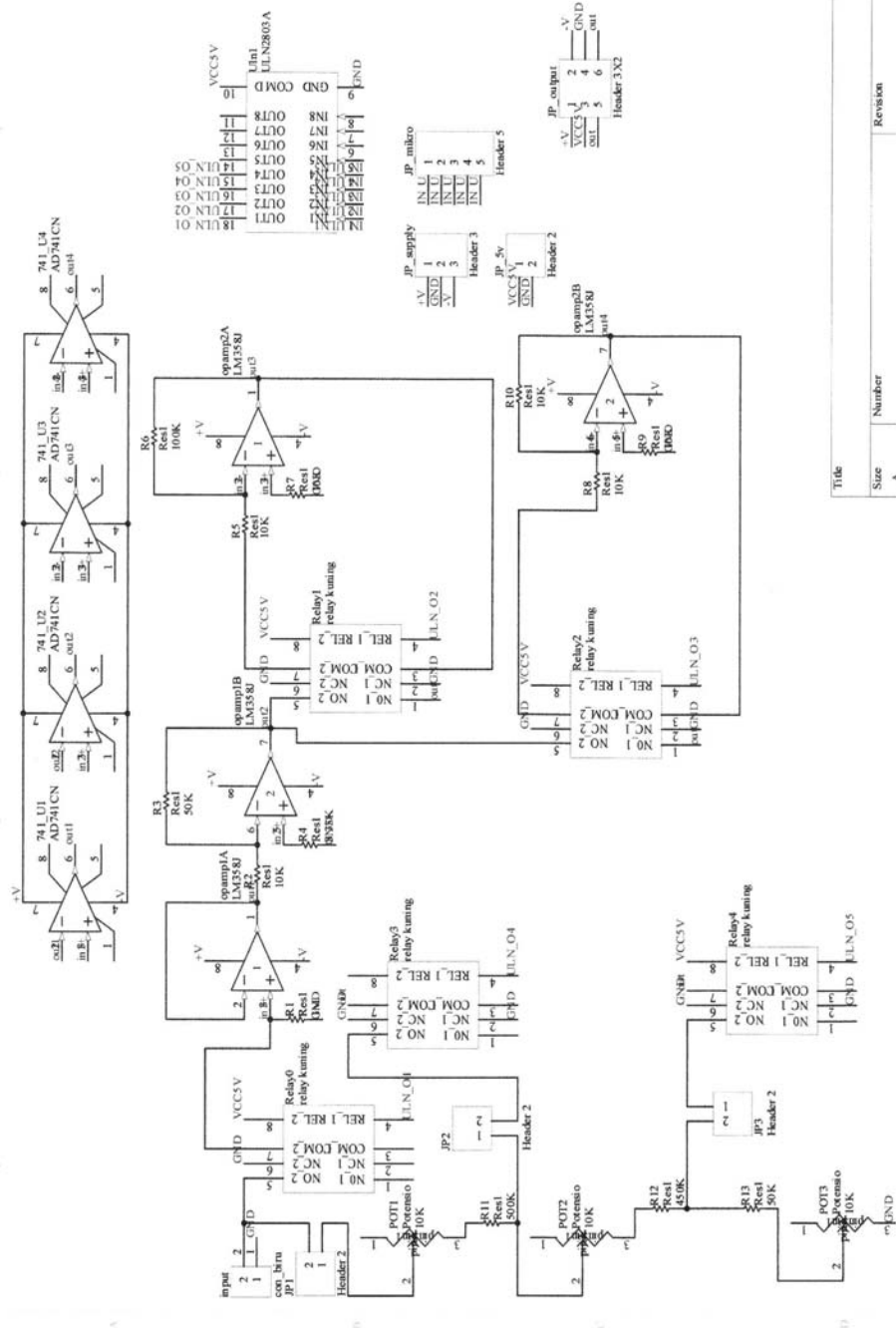
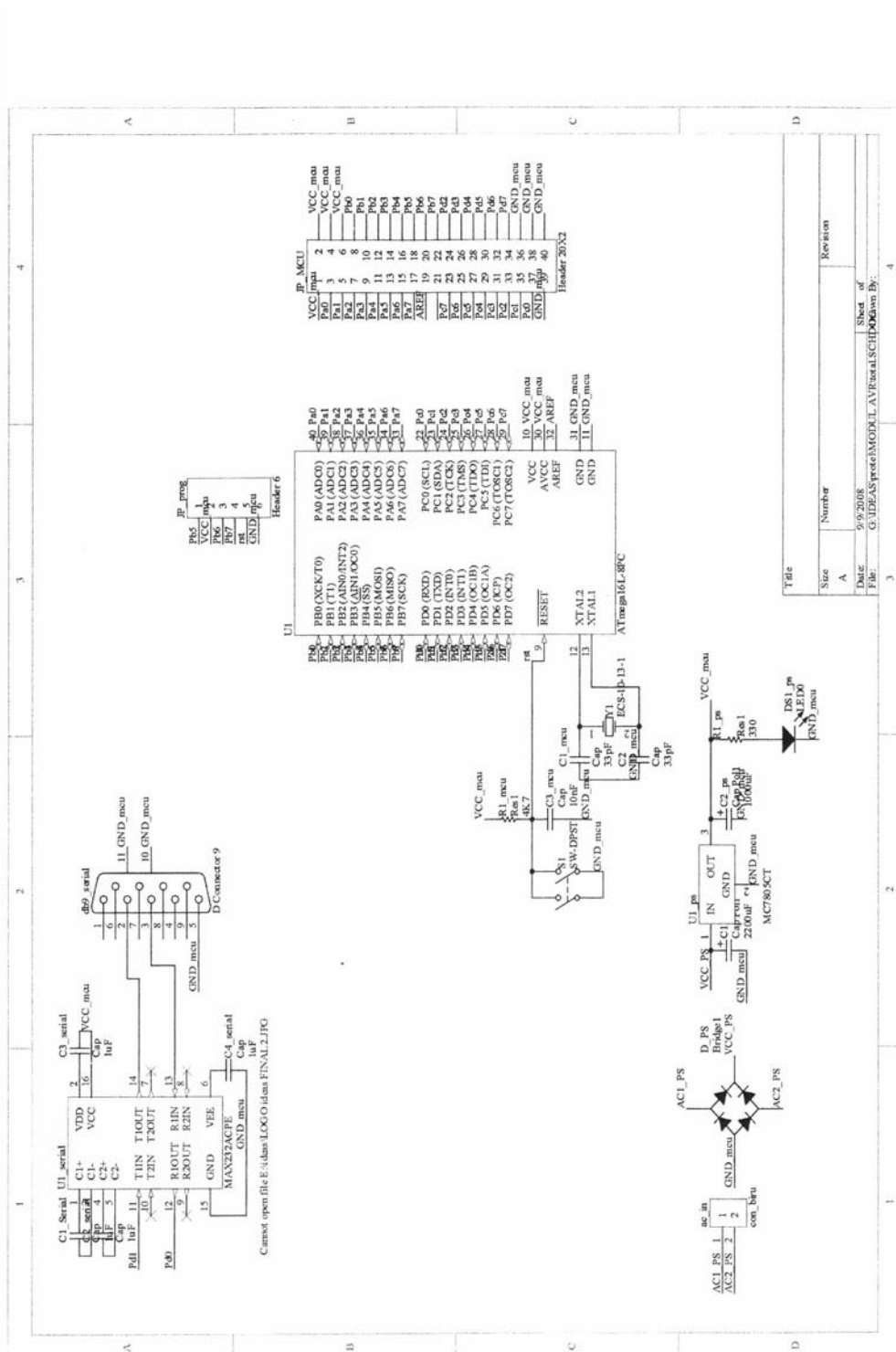


LAMPIRAN A

RANGKAIAN SKEMATIK PERANGKAT KERAS





LAMPIRAN B

LIST PROGRAM MIKROKONTROLER

```
include <Arduino.h>

//=====
// serial
//=====

#define F_CPU 16000000
#define BAUD_RATE 57600
#define RX_PIN 17
#define TX_PIN 18
#define RX_BUFFER_SIZE 256
#define TX_BUFFER_SIZE 256

//=====

void setup() {
  pinMode(RX_PIN, INPUT);
  pinMode(TX_PIN, OUTPUT);

  Serial.begin(BAUD_RATE);
  Serial.println("Serial Test");
}

void loop() {
  while (Serial.available()) {
    char inChar = Serial.read();
    Serial.print(inChar);
  }
}
```

```
hd 16,0x00
```

```
out port,16
```

```
hd 16,0xff
```

```
out port,16
```

```
hd 16,0x00 ;inisialisasi port b low
```

```
out port,16
```

```
call inis_uart
```

```
call inis_smb
```

```
rek_fanuki:
```

```
call uart_tx
```

```
jmp rek_fanuki
```

```
brn rek_fanuki
```

```
hd 0x00,5
```

```
call uart_tx
```

```
jmp setting_fanuki
```

```
setting_fanuki:
```

```
call uart_tx
```

```
mov R0,0x00
```

```
jmp R0,0
```

```
brn p1
```

```
jmp R0,0
```

```
brn p2
```

```
jmp R0,1
```

```
brn p3
```

```
jmp R0,2
```

```
brn p4
```

```
jmp setting_fanuki
```

sub program pemilihan kanal

```
=====
;inisialisasi pinb Program
=====
```

```
p1:
```

```
push 16
```

```
hd 16,0xff
```

out 4444.16

out 4442.16

out 4444.16

Rossi, Poni. 1984. *Keaganan nilai logika 9*

td 16.0x09

out 4444.16

out 4442.16

out 4444.16

td 16.0x06 Mengaktifkan penguatan 30x

out portb.16 Kanal 1 dan 2 P.00, P.01, P.02, P.06

td 16.0x06 Mengaktifkan penguatan 30x

out portb.16 Kanal 3 P.02, P.03

td 16.0x06 Mengaktifkan penguatan 30x

out portb.16 Kanal 4 P.02, P.03

td 16.0x06

out 4444.16

pop 16

jmp ke 300.6

2.

push 16

td 16.0x06

out 4444.16

out 4442.16

out 4444.16

td 16.0x06 Mengaktifkan penguatan 3x

out portb.16 Kanal 1 dan 2 P.02, P.03

td 16.0x06 Mengaktifkan penguatan 3x

out portb.16 Kanal 3 P.03

td 16.0x06 Mengaktifkan penguatan 3x

out portb.16 Kanal 4 P.03

td 16.0x06

out 4444.16

pop 16

jmp ke 300.6

kembali:

ring setting kembali

pa:

push 16

ldr 16, 0xFF

out 0x04, 16

out 0x02, 16

out 0x01, 16

ldr 16, 0x208 : Mengaktifkan portan 0, 3x

out port, 16 : Janel 1, : P 33,

ldr 16, 0x221 : Mengaktifkan portan 0, 3x

out port, 16 : Janel 2 dan 3 : P 62, P 63,

ldr 16, 0x229 : Mengaktifkan portan 0, 3x

out port, 16 : Janel 4 : P 65,

ldr 16, 0x235

in all user, 16

pop 16

ring kean 0x06

pb:

push 16

ldr 16, 0xFF

out 0x04, 16

out 0x02, 16

out 0x01, 16

ldr 16, 0x203 : Mengaktifkan portan 0, 3x

out port, 16 : Janel 1 : P 34,

ldr 16, 0x222 : Mengaktifkan portan 0, 3x

out port, 16 : Janel 2 dan 3 : P 61, P 62,

ldr 16, 0x229 : Mengaktifkan portan 0, 3x

out port, 16 : Janel 4 : P 66,

ldr 16, 0x235

in all user, 16


```
pop r16  
jmp hwi_06
```

```
=====
```

```
:sub program get AD_0
```

```
=====
```

```
hwi_06:
```

```
call uart_00  
mov r22,r25h  
shl r22,l  
jmp loop  
nop  
shl r22,l  
jmp loop  
nop  
jmp hwi06b
```

```
loop:
```

```
nop  
nop  
shl uart_00  
jmp rdt0  
in r25h,rtd  
jmp hwi_06
```

```
rdt0:
```

```
shl r22,l  
jmp rdt  
nop  
call AD_01
```

```
rdt1:
```

```
shl r22,l  
jmp rdt2  
nop  
call AD_01
```

```
rdt2:
```

```
shl r22,l  
jmp rdt3  
nop  
call AD_02
```

```
rdt3:
```

```
shl r22,l
```

```
jmp loop  
nop  
jmp $D.03
```

=====

: Pemilihan multiplier masalah

=====

\$D.03:

```
ldr r6, #0x100000  
mli mdata, r6  
push r6  
  
ldr r6, #0x00000111  
mli mdata, r6  
str r6  
  
ldr r6, #0x00000111  
mli mdata, r6  
  
ldr r6, #0x01  
call mdata, r6  
call pin_data, r6  
  
pop r6  
ret
```

pin_data_r6:

```
str mdata, r6
```

hitung_r6:

```
str mdata, r6  
jmp hitung_r6  
in data_r6_1, r6  
  
call pin_r6  
ret
```

pin_r6:

```
mov r6, data_r6_1  
call mdata, r6  
ret
```

\$D.04:

```
ldr r6, #0x000001
```

```
out wdtmr,16  
push 16  
ldr r6,0x00000011  
out wdtmr,16  
str r6,  
ldr r6,0x00000011  
out wdtmr,16
```

```
ldr r6,r6r2  
vcall wari_0  
vcall get_data_wdt1  
  
pop 16  
ret
```

```
get_data_wdt1:  
str wdtmr,wdt0  
unwgtm_wdt1:  
str wdtmr,wdt1  
vmp unwgtm_wdt1  
in data_wdt_1,wdt1  
  
vcall print_wdt1  
ret
```

```
print_wdt1:  
mov r6,r6r2,wdt_1  
vcall wari_0  
ret
```

3.2.2.

```
ldr r6,0x00100010  
out wdtmr,16  
push 16  
ldr r6,0x00000011  
out wdtmr,16  
str r6,  
ldr r6,0x00000011  
out wdtmr,16
```

```
ldr r6,r6r3  
vcall wari_0  
vcall get_data_wdt1
```

pop r16

ret

get_data_sud2:

ldi r25,0x00

mullopp_sud2:

ldi r25,0x00

jmp mullopp_sud2

in_data_sud_1_sud3

icall prin_sud2

ret

prin_sud2:

mov r25,in_data_sud_1

icall matri_x

ret

00_02:

ldi r16,0x00000011

ori r25,r16

push r16

ldi r16,0x00000011

ori r25,r16

010_02

ldi r16,0x00000011

ori r25,r16

ldi r25,0

icall matri_x

icall get_data_sud3

pop r16

jmp loop

get_data_sud3:

ldi r25,0x00

mullopp_sud3:

ldi r25,0x00

jmp mullopp_sud3

in_data_sud_1_sud3

```
void print_suhu
```

```
{
```

```
print_suhu:
```

```
mov r0, data_suhu_1
```

```
void main_0
```

```
{
```

```
=====
```

```
:SD_6
```

```
=====
```

```
init_suhu:
```

```
push r0
```

```
ldr r0, #0x00000000
```

```
str r0, #0
```

```
ldr r0, #0x00000011
```

```
str r0, #4
```

```
pop r0
```

```
}
```

```
=====
```

```
:SD_6 main 1
```

```
=====
```

```
/*
```

```
get_data_suhu:
```

```
ldr r0, #0x00000000
```

```
str r0, #0
```

```
push r0
```

```
ldr r0, #0x00000000
```

```
str r0, #4
```

```
ldr r0, #0
```

```
ldr r0, #0x00000000
```

```
str r0, #8
```

```
void get_data_suhu
```

```
{
```

```
}
```

```

get_data_sdt:
    shi sdram, sdt

array_sdt:
    shi sdram, array_sdt
    jmp array_sdt
    in data_sdt, sdt_b

    rcall print_sdt

ret

print_sdt:
    mov r3, in_data_sdt_b

    rcall array_a

=====
; S.T.R.I.L.
=====

array_a:
    shi sdram, array_a
    jmp array_a
    in r3, r3, array_a

ret

array_b:
    shi sdram, array_b
    jmp array_b
    out r3, r3, array_b

ret

init_array:
    ldi r16, high(addr_sdt_a)
    out addr_r16

    ldi r16, low(addr_sdt_a)
    out addr_r16

    ldi r16, (1, r2r)
    out addr_r16

    ldi r16, (1, r2r) * 2
    out addr_r16

    ldi r16, (1, r2r) * 3
    out addr_r16

ret
    
```


LAMPIRAN C

LIST PROGRAM DELPHI



Windows, Manager, Zip, Mail, Class, Graphics, Control, Forms, Desktop,
Core, Calc, File, Calc, Calc, Buttons, & Buttons, Control, & Custom Buttons,
Print, Print, Calc, & Control, Control, & Calc, & Calc, & Calc, & Custom Label, & Print,
& Print, & Group, & Print, & Print, & Print, & Print, & Custom Label, & Calc,
& Print, & Print, & Custom Menu Manager, & Control, & Calc, & Calc, & Calc, & Calc,
& Calc, & Calc, & Calc, & Calc, & Calc, & Calc, & Calc, & Calc, & Calc, & Calc,
& Calc, & Calc, & Calc, & Calc, & Calc, & Calc, & Calc, & Calc, & Calc, & Calc,



Print Form - Print Form

Print Form - Print Form

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Print Form - Print Form

Print Form - Print Form

Print Form - Print Form

Print Form - Print Form

Print Form - Print Form

Beault: F Beault

Beault: F Beault

Beault: F Beault

Beault: F Beault

Beault: F Beault

Beault: F Beault

Beault: F Beault

Beault: F Beault

Beault: F Beault

Beault: F Beault

Beault: F Beault

Beault: F Beault

Beault: F Beault

Beault: F Beault

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Beault: F Beault

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Beault: F Beault

Beault: F Beault

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Beault: F Beault

Beault: F Beault

Beault: F Beault

Beault: F Beault

Beault: F Beault

Beault: F Beault

Beault: F Beault

Beault: F Beault

Beault: F Beault

Beault: F Beault

Beault: F Beault

Beault: F Beault

Beault: F Beault

Beault: F Beault

Beault: F Beault

Beault: F Beault

Beault: F Beault

Beault: F Beault

Image 1: Sings

Image 1: Sings

Image 1: Sings

Image 1: Sings

Image 1: Sings

Image 1: Sings

Image 1: Sings

Image 1: Sings

Image 1: Sings

Image 1: Sings

Image 1: Sings

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Image 1: Sings

3. 30:07:36*

```
// ---- latihan program xygraph (manipulasi teks) ---- //
```

1. Program: Print Bilal Print (tombol: 945736)

layar: bilalbilal

2. Program: bilal

layar: bilal

```
xygraph('bilalbilal'; bilalbilal; bilalbilal);
```

```
xygraph(0,0,0,0,0,0,0,0,0,0,0,0);
```

```
xygraph(0,0,0,0,0,0,0,0,0,0,0,0);
```

```
xygraph(0,0,0,0,0,0,0,0,0,0,0,0);
```

```
xygraph(0,0,0,0,0,0,0,0,0,0,0,0);
```

```
xygraph(0,0,0,0,0,0,0,0,0,0,0,0);
```

```
xygraph(0,0,0,0,0,0,0,0,0,0,0,0);
```

```
xygraph(0,0,0,0,0,0,0,0,0,0,0,0);
```

```
xygraph(0,0,0,0,0,0,0,0,0,0,0,0);
```

```
xygraph(0,0,0,0,0,0,0,0,0,0,0,0);
```

```
xygraph(0,0,0,0,0,0,0,0,0,0,0,0);
```

```
xygraph(0,0,0,0,0,0,0,0,0,0,0,0);
```

```
xygraph(0,0,0,0,0,0,0,0,0,0,0,0);
```

3. Program: bilalbilal

layar: bilalbilal

4. Program: bilal

layar: bilalbilalbilalbilalbilal

5. Program: bilalbilal

layar: bilalbilalbilalbilalbilalbilal

6. Program: bilalbilal

layar: bilalbilalbilalbilalbilalbilal

// program manipulasi gambar (latihan xygraph)

1. Program: bilalbilal bilal bilal bilal (tombol: 945736)

layar: bilalbilal bilal bilal bilal

2. Program: bilalbilal

layar: bilalbilal bilal bilal bilal

3. Program: bilalbilal

layar: bilalbilal bilal bilal bilal

4. Program: bilalbilal

layar: bilalbilal bilal bilal bilal

5. Program: bilalbilal

layar: bilalbilal bilal bilal bilal

6. Program: bilalbilal

layar: bilalbilal bilal bilal bilal

7. Program: bilalbilal

layar: bilalbilal bilal bilal bilal

8. Program: bilalbilal

layar: bilalbilal bilal bilal bilal

9. Program: bilalbilal

layar: bilalbilal bilal bilal bilal

10. Program: bilalbilal

layar: bilalbilal bilal bilal bilal

11. Program: bilalbilal

layar: bilalbilal bilal bilal bilal

12. Program: bilalbilal

layar: bilalbilal bilal bilal bilal

pat-5:
pat-5B:
~~pat-5B~~
pat-5
~~pat-5~~
~~pat-5~~
pat-5 (P. Ferrel's 'pat-5' (Lamp) (Lamp) (Lamp))

Lamp:
pat-5
Lamp (Lamp) (Lamp)

pat-5
pat-5

pat-1:
pat-1:
~~pat-1~~
pat-1
pat-2:
pat-2B:
~~pat-2B~~

pat-3:
pat-3B:
~~pat-3B~~

pat-4:
pat-4B:
~~pat-4B~~

pat-5:
pat-5B:
~~pat-5B~~

pat-6:
~~pat-6~~
~~pat-6~~

pat-6 (P. Ferrel's 'pat-6' (Lamp) (Lamp) (Lamp))

Lamp:
pat-6
Lamp (Lamp) (Lamp)

pat-1:
pat-1:
~~pat-1~~
pat-2:
pat-2B:
~~pat-2B~~

Filter dan

Objek

```
ak=sementra(mata,mawar,gunung,gunung);  
jd=judul "sementra";  
xl=300*(panjang * dx);  
paintbox.Canvas.Pen.Color=Color;  
paintbox.Canvas pen.Width=1;  
paintbox.Canvas.Line To((pdx dx) pdy dx);
```

ak=ak+1;

dx=dx+1;

dx=dx+1;

Filter

Filter dan

Objek

```
ak=sementra(mata,mawar,gunung,gunung);  
jd=judul "sementra";  
xl=300*(panjang * dx * 2);  
paintbox.Canvas.Pen.Color=Color;  
paintbox.Canvas pen.Width=1;  
paintbox.Canvas.Line To((pdx dx) pdy dx);
```

ak=ak+1;

dx=dx+1;

dx=dx+1;

Filter

Filter

dx=dx+1;

dx=dx+1;

Filter

ak=ak+1;

Filter

Filter

Filter

////

// -ingat Program akan menampilkan garis basis warna -//

// Ketik di program dengan sebagai program program gambar //

Form1.Paint Paint Back MouseDown(Sender: Object): Button: Form Mouse Button:

Style: Style Name: K. J. Tanjung;

Objek

ygipandip := 1;

ygmandip(Canvas.Left, x, y);

ygipandip := 0 and ygmandip := 0 and

not (in List in list) dan

Objek

Edil := Canvas.Left(Canvas.Width) 6;

Edil := 0;

kanan := 0;

Filter

Filter

Form1.Paint Paint Back Mouse.MouseDown(Sender: Object): Style: Style: Style Name: K.



~~#####~~ *F:Parent, Parent, Child (Variables: Physics);*

~~~~~~~~~

~~F:Gen:Parent, Generalized = true den~~

~~Gen:Parent, Child~~

~~~~~~~~~

~~Gen:Parent, Spine;~~

~~~~~~~~~

~~#####~~ *F:Parent, Gen:ParentSpine (Variables: Physics);*

~~~~~~~~~

~~s:Parent, Capitan => Child;~~

~~>parent, Wires:Yin(Y);~~

~~inherit, Enabled=var;~~

~~~~~~~~~

~~// print some stuff into console before error //~~

~~#####~~ *F:Parent, Gen:Parent, Re:Child (Variables: Physics, Genus, Species);*

~~~~~~~~~

~~>parent, Root(Child);~~

~~~~~~~~~

~~re:Child;~~

~~re:Child, Genus=>Genus(Y);~~

~~#####~~

~~re:Child = 1000 den~~

~~inherit, Child~~

~~re:Child = 1001 den~~

~~re:Child~~

~~re:Child~~

~~re:Child~~

~~re:Child~~

~~// ..... //~~

~~// print instructions and variables serial: P, R, S //~~

~~#####~~ *F:Parent, Gen:Parent, Genus (Variables: Physics);*

~~~~~~~~~

~~F:Parent, Genus = true den~~

~~~~~~~~~

~~s:Parent, Capitan => Spine;~~

~~inherit, Wires:Yin(Y);~~

~~~~~~~~~

~~~~~~~~~

~~#####~~ *F:Parent, Parent2, Child (Variables: Physics);*

~~~~~~~~~

~~Gen:Parent, Genus:Yin(Y);~~

~~~~~~~~~

~~// ..... //~~

~~// print the basic variable //~~

~~#####~~ *F:Parent, Genus:Yin (Variables: Physics);*

~~~~~~~~~

~~Yin, Enabled=Yin;~~

~~Yin, Enabled=Yin;~~

~~Yin, Enabled=Yin;~~

Case

$\mathbb{E}[X_1 | \text{From } \text{Index} = 0 \text{ then}]$

Case

$\mathbb{E}[X_2 | \text{From } \text{Index} = 0]$

$\mathbb{E}[X_3 | \text{From } \text{Index} = 0]$

$\mathbb{E}[X_4 | \text{From } \text{Index} = 0]$

Case

$\mathbb{E}[X_1 | \text{From } \text{Index} = 1 \text{ then}]$

Case

$\mathbb{E}[X_2 | \text{From } \text{Index} = 1]$

$\mathbb{E}[X_3 | \text{From } \text{Index} = 1]$

$\mathbb{E}[X_4 | \text{From } \text{Index} = 1]$

Case

$\mathbb{E}[X_1 | \text{From } \text{Index} = 2 \text{ then}]$

Case

$\mathbb{E}[X_2 | \text{From } \text{Index} = 2]$

$\mathbb{E}[X_3 | \text{From } \text{Index} = 2]$

$\mathbb{E}[X_4 | \text{From } \text{Index} = 2]$

Case

$\mathbb{E}[X_1 | \text{From } \text{Index} = 3 \text{ then}]$

Case

$\mathbb{E}[X_2 | \text{From } \text{Index} = 3]$

$\mathbb{E}[X_3 | \text{From } \text{Index} = 3]$

$\mathbb{E}[X_4 | \text{From } \text{Index} = 3]$

Case

Case

$\mathbb{E}[X_1 | \text{From } \text{Index} = \text{Change}(\text{Index}, \text{Step})]$

Case

$\mathbb{E}[X_1 | \text{From } \text{Index} = 0 \text{ then}]$

Case

Case

$\mathbb{E}[X_1 | \text{From } \text{Index} = 1 \text{ then}]$

Case

Case

$\mathbb{E}[X_1 | \text{From } \text{Index} = 2 \text{ then}]$

Case

Case

$\mathbb{E}[X_1 | \text{From } \text{Index} = 3 \text{ then}]$

Case

Case

$\mathbb{E}[X_1 | \text{From } \text{Index} = \text{Change}(\text{Index}, \text{Step})]$

Case

$\mathbb{E}[X_1 | \text{From } \text{Index} = 0 \text{ then}]$

Case

Case

$\mathbb{E}[X_1 | \text{From } \text{Index} = 1 \text{ then}]$

Case

~~program~~ P.Pascal. Bakuari (A.1.1) (Jember: P.1992);

~~objek~~

ada:=ada1+ada2+ada3+ada4;

komparasi. (A.1.1) (Jember: P.1992);

ada1:=10;

ada2:=5;

ada3:=3;

ada4:=1;

ada5:=1;

ada6:=1;

ada7:=1;

ada8:=1;

~~program~~

komparasi. (A.1.1) (Jember: P.1992);

ada1:=10;

ada2:=5;

ada3:=3;

ada4:=1;

ada5:=1;

ada6:=1;

ada7:=1;

ada8:=1;

ada9:=1;

ada10:=1;

ada11:=1;

ada12:=1;

ada13:=1;

ada14:=1;

ada15:=1;

~~objek~~

~~program~~

~~program~~ P.Pascal. Bakuari (A.1.1) (Jember: P.1992);

~~objek~~

ada:=ada1+ada2+ada3+ada4;

ada1:=10;

ada2:=5;

ada3:=3;

ada4:=1;

// ... Program program menggambar gambar ... //

~~program~~ P.Pascal. Bakuari (A.1.1) (Jember: P.1992);

~~objek~~

ada:=ada1+ada2+ada3+ada4;

ada1:=10;

ada2:=5;

ada3:=3;

ada4:=1;

Septuaginta, Volumen 1
part-122

Septuaginta, Volumen 1
part-222

Septuaginta, Volumen 1
part-222

Septuaginta, Volumen 1
part-33

Septuaginta, Volumen 2
part-122

Septuaginta, Volumen 3
part-122

Septuaginta, Volumen 4
part-222

Septuaginta, Volumen 5
part-222

Septuaginta
part-33

Septuaginta

Septuaginta, Volumen 1 (Septuaginta, Volumen 1)

Septuaginta

Septuaginta, Volumen 1
part-33

Septuaginta, Volumen 2
part-122

Septuaginta, Volumen 3
part-122

Septuaginta, Volumen 4
part-222

Septuaginta, Volumen 5
part-222

Septuaginta, Volumen 1
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Septuaginta, Volumen 2
part-122

Septuaginta, Volumen 3
part-122

Septuaginta, Volumen 4
part-222

Septuaginta, Volumen 5
part-222

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part-33

Septuaginta

Septuaginta, Volumen 1 (Septuaginta, Volumen 1)

Septuaginta

Septuaginta, Volumen 1
part-33

Septuaginta, Volumen 2
part-122

paß-159

~~paß~~ Paßbüchel, Vollen-3 des

paß-162

~~paß~~ Paßbüchel, Vollen-4 des

paß-252

~~paß~~ Paßbüchel, Vollen-5 des

paß-252

~~paß~~ Paßbüchel, Vollen-1 des

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~~paß~~ Paßbüchel, Vollen-2 des

paß-159

~~paß~~ Paßbüchel, Vollen-3 des

paß-162

~~paß~~ Paßbüchel, Vollen-4 des

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~~paß~~ Paßbüchel, Vollen-5 des

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~~paß~~ Paßbüchel, Vollen-1 des (Länge (Länge: 90/100))

~~paß~~

~~paß~~ Paßbüchel, Vollen-1 des

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paß-252

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// //

~~paß~~ Paßbüchel, Vollen-1 des (Länge (Länge: 90/100))

~~paß~~

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LAMPIRAN D

FOTO ALAT DAN PERANGKAT LUNAK

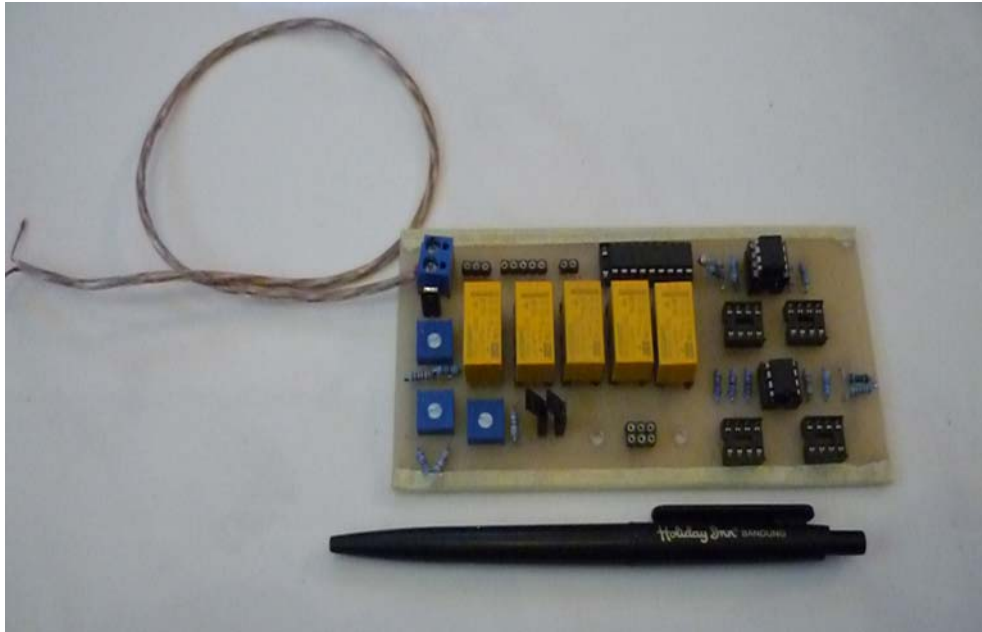
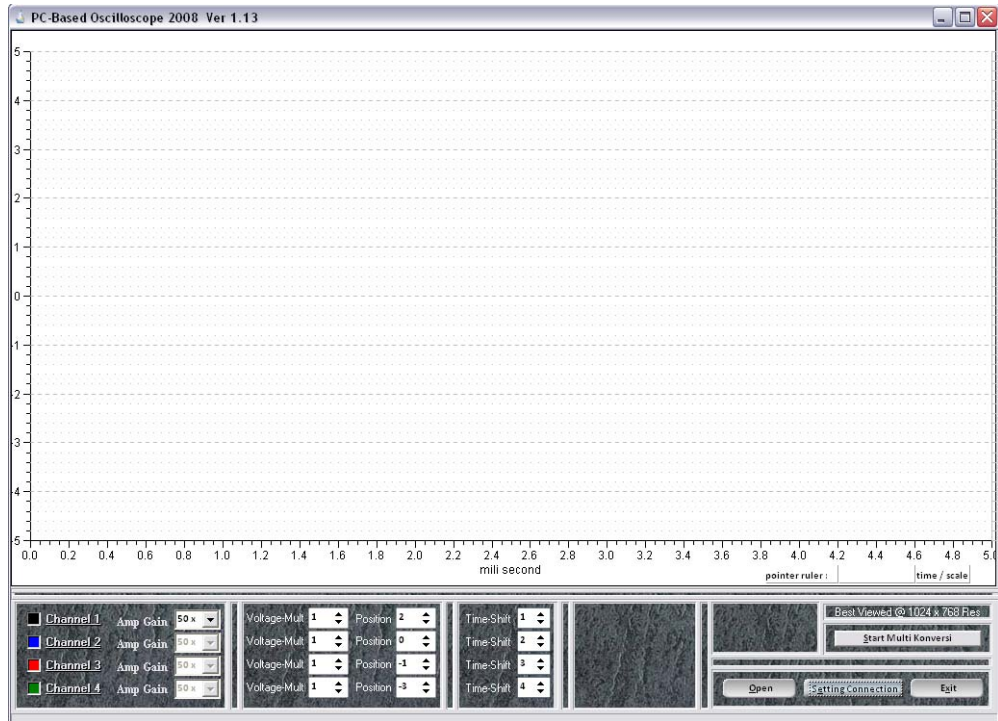


Foto Rangkaian Perangkat Lunak Polarisasi



Foto Rangkaian Digital Mikrokontroler Dengan 8255



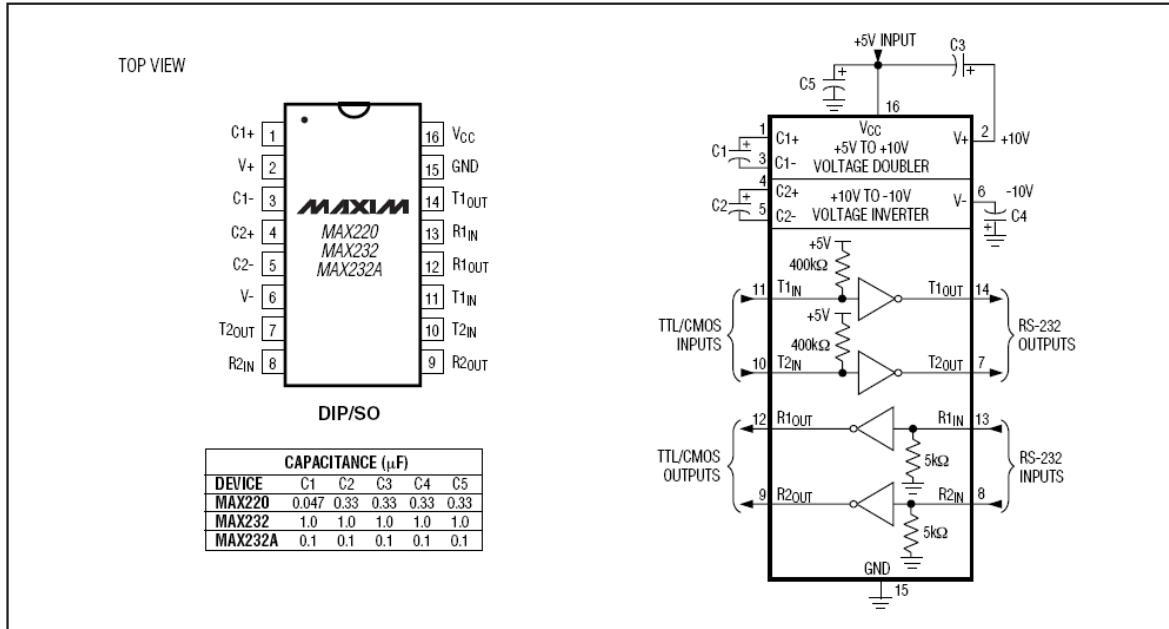
Empire Engineering Limited Pte Ltd

LAMPIRAN E

DATASHEET KOMPONEN



+5V-Powered, Multichannel RS-232 Drivers/Receivers



ELECTRICAL CHARACTERISTICS—MAX220/222/232A/233A/242/243

($V_{CC} = +5V \pm 10\%$, $C1-C4 = 0.1\mu\text{F}$, MAX220, $C1 = 0.047\mu\text{F}$, $C2-C4 = 0.33\mu\text{F}$, $T_A = T_{MIN}$ to T_{MAX} , unless otherwise noted.)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS	
RS-232 TRANSMITTERS						
Output Voltage Swing	All transmitter outputs loaded with $3k\Omega$ to GND	± 5	± 8		V	
Input Logic Threshold Low			1.4	0.8	V	
Input Logic Threshold High	All devices except MAX220 MAX220: $V_{CC} = 5.0V$	2	1.4		V	
Logic Pullup/Input Current	All except MAX220, normal operation $\overline{\text{SHDN}} = 0V$, MAX222/MAX242, shutdown, MAX220		5	40	μA	
Output Leakage Current	$V_{CC} = 5.5V$, $\overline{\text{SHDN}} = 0V$, $V_{OUT} = \pm 15V$, MAX222/MAX242		± 0.01	± 10	μA	
	$V_{CC} = \overline{\text{SHDN}} = 0V$		± 0.01	± 10	μA	
	MAX220, $V_{OUT} = \pm 12V$			± 25	μA	
Data Rate			200	116	kbps	
Transmitter Output Resistance	$V_{CC} = V_+ = V_- = 0V$, $V_{OUT} = \pm 2V$	300	10M		Ω	
Output Short-Circuit Current	$V_{OUT} = 0V$	± 7	± 22		mA	
	MAX220			± 60	mA	
RS-232 RECEIVERS						
RS-232 Input Voltage Operating Range				± 30	V	
				MAX220 ± 25	V	
RS-232 Input Threshold Low	$V_{CC} = 5V$	All except MAX243 $R2_{IN}$ MAX243 $R2_{IN}$ (Note 4)	0.8 -3	1.3	V	
RS-232 Input Threshold High	$V_{CC} = 5V$	All except MAX243 $R2_{IN}$ MAX243 $R2_{IN}$ (Note 4)		1.8 -0.5	2.4 -0.1	V

PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
RS-232 Input Hysteresis	All except MAX220/MAX243, V _{CC} = 5V, no hysteresis in SHDN		0.2	0.5	1.0	V
	MAX220		0.3			
	MAX243		1			
RS-232 Input Resistance	T _A = +25°C (MAX220)		3	5	7	kΩ
			3	5	7	
TTL/CMOS Output Voltage Low	I _{OUT} = 3.2mA		0.2		0.4	V
	I _{OUT} = 1.6mA (MAX220)		0.4			
TTL/CMOS Output Voltage High	I _{OUT} = -1.0mA		3.5	V _{CC} - 0.2		V
TTL/CMOS Output Short-Circuit Current	Sourcing V _{OUT} = GND		-2	-10		mA
	Sinking V _{OUT} = V _{CC}		10	30		
TTL/CMOS Output Leakage Current	SHDN = V _{CC} or EN = V _{CC} (SHDN = 0V for MAX222), 0V ≤ V _{OUT} ≤ V _{CC}		±0.05		±10	μA
EN Input Threshold Low	MAX242		1.4		0.8	V
EN Input Threshold High	MAX242		2.0	1.4		V
Operating Supply Voltage			4.5	5.5		V
V _{CC} Supply Current (SHDN = V _{CC}), Figures 5, 6, 11, 19	No load	MAX220	0.5		2	μA
		MAX222/MAX232A/MAX233A/ MAX242/MAX243	4		10	
	3kΩ load both inputs	MAX220	12			
		MAX222/MAX232A/MAX233A/ MAX242/MAX243	15			

Shutdown Supply Current	MAX222/ MAX242	T _A = +25°C	0.1	10		μA
		T _A = 0°C to +70°C	2	50		
		T _A = -40°C to +85°C	2	50		
		T _A = -55°C to +125°C	35	100		
SHDN Input Leakage Current	MAX222/MAX242		±1		μA	
SHDN Threshold Low	MAX222/MAX242		1.4		0.8	V
SHDN Threshold High	MAX222/MAX242		2.0	1.4		V
Transition Slew Rate	C _L = 50pF to 2500pF, R _L = 3kΩ to 7kΩ, V _{CC} = 5V, T _A = +25°C, measured from +3V to -3V or -3V	MAX222/MAX232A/MAX233/ MAX242/MAX243	6	12	30	V/μs
		MAX220	1.5	3	30.0	
Transmitter Propagation Delay TLL to RS-232 (Normal Operation), Figure 1	t _{PHLT}	MAX222/MAX232A/MAX233/ MAX242/MAX243	1.3		3.5	μs
		MAX220	4		10	
	t _{PLHT}	MAX222/MAX232A/MAX233/ MAX242/MAX243	1.5		3.5	
		MAX220	5		10	

Electrical Characteristics

ATmega8535(L)

Absolute Maximum Ratings*

Operating Temperature	-55°C to +125°C
Storage Temperature	-65°C to +150°C
Voltage on any Pin except RESET with respect to Ground	-0.5V to $V_{CC}+0.5V$
Voltage on RESET with respect to Ground.....	-0.5V to +13.0V
Maximum Operating Voltage	6.0V
DC Current per I/O Pin	40.0 mA
DC Current V_{CC} and GND Pins	200.0 mA

*NOTICE: Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



DC Characteristics

$T_A = -40^{\circ}\text{C}$ to 85°C , $V_{CC} = 2.7V$ to $5.5V$ (unless otherwise noted)

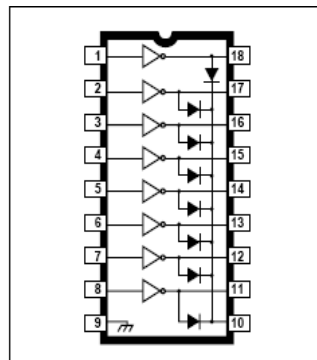
Symbol	Parameter	Condition	Min	Typ	Max	Units
V_{IL}	Input Low Voltage	Except XTAL1 pin	-0.5		$0.2 V_{CC}^{(1)}$	V
V_{IL1}	Input Low Voltage	XTAL1 pin, External Clock Selected	-0.5		$0.1 V_{CC}^{(1)}$	V
V_{IH}	Input High Voltage	Except XTAL1 and RESET pins	$0.6 V_{CC}^{(2)}$		$V_{CC} + 0.5$	V
V_{IH1}	Input High Voltage	XTAL1 pin, External Clock Selected	$0.8 V_{CC}^{(2)}$		$V_{CC} + 0.5$	V
V_{IH2}	Input High Voltage	RESET pin	$0.9 V_{CC}^{(2)}$		$V_{CC} + 0.5$	V
V_{OL}	Output Low Voltage ⁽³⁾ (Ports A,B,C,D)	$I_{OL} = 20\text{ mA}$, $V_{CC} = 5V$ $I_{OL} = 10\text{ mA}$, $V_{CC} = 3V$			0.7 0.5	V
V_{OH}	Output High Voltage ⁽⁴⁾ (Ports A,B,C,D)	$I_{OH} = -20\text{ mA}$, $V_{CC} = 5V$ $I_{OH} = -10\text{ mA}$, $V_{CC} = 3V$	4.2 2.2			V
I_{IL}	Input Leakage Current I/O Pin	$V_{CC} = 5.5V$, pin low (absolute value)			1	μA
I_{IH}	Input Leakage Current I/O Pin	$V_{CC} = 5.5V$, pin high (absolute value)			1	μA
R_{RST}	Reset Pull-up Resistor		30		60	$k\Omega$
R_{PU}	I/O Pin Pull-up Resistor		20		50	$k\Omega$

Symbol	Parameter	Condition	Min	Typ	Max	Units	
I_{CC}	Power Supply Current	Active 4 MHz, $V_{CC} = 3V$ (ATmega8535L)		4		mA	
		Active 8 MHz, $V_{CC} = 5V$ (ATmega8535)		14		mA	
		Idle 4 MHz, $V_{CC} = 3V$ (ATmega8535L)		3		mA	
		Idle 8 MHz, $V_{CC} = 5V$ (ATmega8535)		10		mA	
	Power-down mode ⁽⁵⁾	WDT enabled, $V_{CC} = 3V$			< 10		μA
		WDT disabled, $V_{CC} = 3V$			< 3		μA
V_{ACIO}	Analog Comparator Input Offset Voltage	$V_{CC} = 5V$ $V_{in} = V_{CC}/2$			40	mV	
I_{ACLK}	Analog Comparator Input Leakage Current	$V_{CC} = 5V$ $V_{in} = V_{CC}/2$	-50		50	nA	
t_{ACID}	Analog Comparator Propagation Delay	$V_{CC} = 2.7V$ $V_{CC} = 4.0V$		750 500		ns	

Types ULx2803A, ULx2803LW, ULx2804A, and ULx2804LW
ELECTRICAL CHARACTERISTICS at +25°C (unless otherwise noted).

Characteristic	Symbol	Test Fig.	Applicable Devices	Test Conditions	Limits			
					Min.	Typ.	Max.	Units
Output Leakage Current	I _{CEX}	1A	All	V _{CE} = 50 V, T _A = 25°C	—	< 1	50	μA
				V _{CE} = 50 V, T _A = 70°C	—	< 1	100	μA
				V _{CE} = 50 V, T _A = 70°C, V _{IN} = 1.0 V	—	< 5	500	μA
Collector-Emitter Saturation Voltage	V _{CE(SAT)}	2	All	I _C = 100 mA, I _B = 250 μA	—	0.9	1.1	V
				I _C = 200 mA, I _B = 350 μA	—	1.1	1.3	V
				I _C = 350 mA, I _B = 500 μA	—	1.3	1.6	V
Input Current	I _{IN(ON)}	3	ULx2803x	V _{IN} = 3.85 V	—	0.93	1.35	mA
			ULx2804x	V _{IN} = 5.0 V	—	0.35	0.5	mA
			ULx2804x	V _{IN} = 12 V	—	1.0	1.45	mA
Input Current	I _{IN(OFF)}	4	All	I _C = 500 μA, T _A = 70°C	50	65	—	μA
Input Voltage	V _{IN(ON)}	5	ULx2803x	V _{CE} = 2.0 V, I _C = 200 mA	—	—	2.4	V
				V _{CE} = 2.0 V, I _C = 250 mA	—	—	2.7	V
				V _{CE} = 2.0 V, I _C = 300 mA	—	—	3.0	V
		ULx2804x	V _{CE} = 2.0 V, I _C = 125 mA	—	—	5.0	V	
			V _{CE} = 2.0 V, I _C = 200 mA	—	—	6.0	V	
			V _{CE} = 2.0 V, I _C = 275 mA	—	—	7.0	V	
				V _{CE} = 2.0 V, I _C = 350 mA	—	—	8.0	V
Input Capacitance	C _{IN}	—	All		—	15	25	pF
Turn-On Delay	t _{PLH}	8	All	0.5 E _{IN} to 0.5 E _{OUT}	—	0.25	1.0	μs
Turn-Off Delay	t _{PHL}	8	All	0.5 E _{IN} to 0.5 E _{OUT}	—	0.25	1.0	μs
Clamp Diode Leakage Current	I _R	6	All	V _R = 50 V, T _A = 25°C	—	—	50	μA
				V _R = 50 V, T _A = 70°C	—	—	100	μA
Clamp Diode Forward Voltage	V _F	7	All	I _F = 350 mA	—	1.7	2.0	V

Complete part number includes prefix to operating temperature range: ULN = -20°C to +85°C, ULQ = -40°C to +85°C and a suffix to identify package style: A = DIP, LW = SOIC.



Dwg. No. A-10,822A

Note that the ULx28xxA series (dual in-line package) and ULx28xxLW series (small-outline IC package) are electrically identical and share a common terminal number assignment.

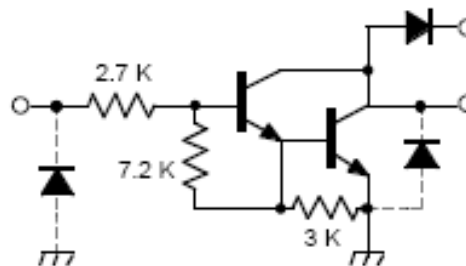
ABSOLUTE MAXIMUM RATINGS

- Output Voltage, V_{CE}
 - (x2803x and x2804x) 50 V
 - (x2823x and x2824x) 95 V
- Input Voltage, V_{IN} 30 V
- Continuous Output Current, I_C 500 mA
- Continuous Input Current, I_{IN} 25 mA
- Power Dissipation, P_D
 - (one Darlington pair) 1.0 W
 - (total package) See Graph
- Operating Temperature Range, T_A
 - Prefix 'ULN' -20°C to +85°C
 - Prefix 'ULQ' -40°C to +85°C
- Storage Temperature Range

2803 THRU 2824
HIGH-VOLTAGE,
HIGH-CURRENT
DARLINGTON ARRAYS

PARTIAL SCHEMATICS

ULx28x3A/LW (Each Driver)



Dwg. FP-052-2

LM136-2.5/LM236-2.5/LM336-2.5V Reference Diode

General Description

The LM136-2.5/LM236-2.5 and LM336-2.5 integrated circuits are precision 2.5V shunt regulator diodes. These monolithic IC voltage references operate as a low-temperature-coefficient 2.5V zener with 0.2Ω dynamic impedance. A third terminal on the LM136-2.5 allows the reference voltage and temperature coefficient to be trimmed easily.

The LM136-2.5 series is useful as a precision 2.5V low voltage reference for digital voltmeters, power supplies or op amp circuitry. The 2.5V make it convenient to obtain a stable reference from 5V logic supplies. Further, since the LM136-2.5 operates as a shunt regulator, it can be used as either a positive or negative voltage reference.

The LM136-2.5 is rated for operation over -55°C to +125°C while the LM236-2.5 is rated over a -25°C to +85°C temperature range.

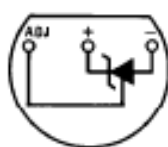
The LM336-2.5 is rated for operation over a 0°C to +70°C temperature range. See the connection diagrams for available packages.

Features

- Low temperature coefficient
- Wide operating current of 400 μA to 10 mA
- 0.2Ω dynamic impedance
- ±1% initial tolerance available
- Guaranteed temperature stability
- Easily trimmed for minimum temperature drift
- Fast turn-on
- Three lead transistor package

Connection Diagrams

TO-92
Plastic Package



TL/H/5715-8

Bottom View

Order Number LM236Z-2.5,
LM236AZ-2.5, LM336Z-2.5 or
LM336BZ-2.5
See NS Package Number Z03A

TO-46
Metal Can Package

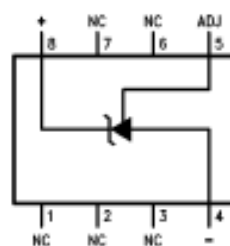


TL/H/5715-20

Bottom View

Order Number LM136H-2.5,
LM136H-2.5/883, LM236H-2.5,
LM136AH-2.5, LM136AH-2.5/883
or LM236AH-2.5
See NS Package Number H03H

SO Package



TL/H/5715-12

Top View

Order Number LM236M-2.5,
LM236AM-2.5, LM336M-2.5
or LM336BM-2.5
See NS Package Number M08A

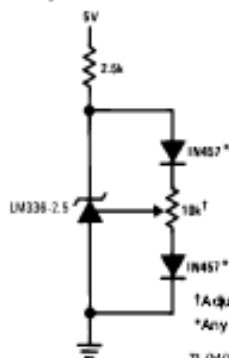
Typical Applications

2.5V Reference



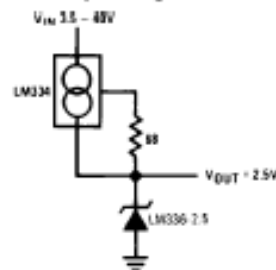
TL/H/5715-9

2.5V Reference with Minimum
Temperature Coefficient

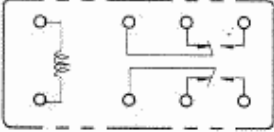
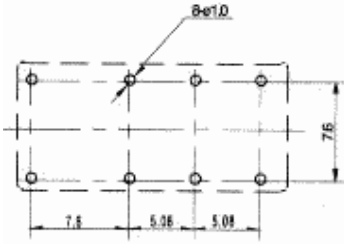


TL/H/5715-10

Wide Input Range Reference



TL/H/5715-11

HRS2/HRS2H	
Dimension (mm) WxDxH	20.8x9.9x12.2
Feature	<ul style="list-style-type: none"> • High Reliability • High Sensitive • FCC Part68 Surge Strength 1500V Available 3000 V (20 μs) • 2 Form C
Contact Material	AuAg
Rated Switching Current	1A 120VAC / 24VDC
Max. Switching Voltage	60VDC 120 VAC
Max. Switching Current	2 A
Max. Switching Power	240 VA 60 W
Min. Switching Current & Voltage	5VDC 1mA
Contact Resistance $m\Omega$, at 6VDC 1A	100
Coil Voltage	3 - 24 VDC
Coil Power Consumption (mW)	150 / 200 / 360
Dielectric Strength between Contacts between Coil and Contacts	1,000V 1,000V
Insulation Resistance $M\Omega$, (at 500VDC)	100
Life Expectancy (ops.) Electrical Mechanical	100,000 10,000,000
Operating Temp. Range	-30 - +60 $^{\circ}$ C
Operating Teme	6 ms
Release Time	4 ms
Structure Washable	Washable
Terminal Shape	P.C.B
Dimension (in mm) Wiring Diagram Drilling Plan	
	

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